

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	759	agc.clm.	US-PGPUB	OR	OFF	2007/05/10 14:47
L2	1018	"automatic gain control".clm.	US-PGPUB	OR	OFF	2007/05/10 14:47
L3	50589	variable.clm.	US-PGPUB	OR	OFF	2007/05/10 14:48
L4	376419	step.clm.	US-PGPUB	OR	OFF	2007/05/10 14:48
L5	1412	1 or 2	US-PGPUB	OR	OFF	2007/05/10 14:48
L6	14	3 same 4 same 5	US-PGPUB	OR	OFF	2007/05/10 14:51
L7	54797	speed.clm.	US-PGPUB	OR	OFF	2007/05/10 14:51
L8	40597	vary\$.clm.	US-PGPUB	OR	OFF	2007/05/10 14:51
L9	17993	gain.clm.	US-PGPUB	OR	OFF	2007/05/10 14:51
L10	11	7 with 8 with 9	US-PGPUB	OR	OFF	2007/05/10 14:51
L11	0	5 and 10	US-PGPUB	OR	OFF	2007/05/10 14:51

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	1033	375/345.ccls.	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/05/10 14:28
L2	1299939	variable	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/05/10 14:28
L3	2772047	step	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/05/10 14:28
L4	534429	gain	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/05/10 14:28
L5	1544	2 with 3 with 4	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/05/10 14:28
L6	47	1 and 5	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/05/10 14:28
L7	20893000	@ad<"20010516"	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/05/10 14:29
L8	22	6 and 7	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/05/10 14:32

## EAST Search History

L9	2693112	speed	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/05/10 14:32
L10	22529	4 with 9	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/05/10 14:33
L11	97	1 and 10	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/05/10 14:33
L12	54	7 and 11	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/05/10 14:33
L13	9228	4 near3 9	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/05/10 14:33
L14	55131	agc	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/05/10 14:33
L15	55569	1 or 14	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/05/10 14:33
L16	528	13 and 15	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/05/10 14:33

## EAST Search History

L17	331	7 and 16	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/05/10 14:34
L18	1950422	vary\$	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/05/10 14:34
L19	48218	9 near3 18	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/05/10 14:35
L20	142	13 same 19	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/05/10 14:35
L21	6	15 and 20	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/05/10 14:35
L22	4	1 and 19	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/05/10 14:36
L23	1152	4 with 9 same 18	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/05/10 14:37
L24	9	1 and 23	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/05/10 14:37

## EAST Search History

L25	7	7 and 24	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/05/10 14:38
L26	1571	455/232.1,234.1,240.1,245.1.ccis.	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/05/10 14:41
L27	8	19 and 26	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/05/10 14:42
L28	3021	"variable step"	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/05/10 14:42
L29	2336	1 or 26	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/05/10 14:42
L30	8	28 and 29	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/05/10 14:42
L31	3	7 and 30	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/05/10 14:43

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Jeon, O.; Fox, R.M.; Myers, B.A.;  
Solid-State Circuits, IEEE Journal of  
Volume 41, Issue 10, Oct. 2006 Page(s):2291 - 2300  
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[Rights and Permissions](#)**2. A VLSI design of dual-loop automatic gain control for dual-mode QAM/VSC**

Muh-Tian Shieh; Kuang-Hu Huang; Cheng-Chang Lu; Chorng-Kuang Wang; W

Circuits and Systems, 1998. ISCAS '98. Proceedings of the 1998 IEEE Interna

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[Rights and Permissions](#)**3. A temperature compensated L-band variable gain amplifier with eight bit**

Wallace, P.W.; Bayruns, J.; Scheinberg, N.; Rachlin, M.; Krautter, H.;

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